

Fig. 2(b)

## (Channel Access Table Example)

212

		/
Channel Address	Physical Address	Memory Offset
0	0	0x000
1	0	0x200
2	0	0x400
3	0	0x600
4	4	0x000
5	4	0x200
6	4	0x400
7	4	0x600
156	156	0x000
157	156	0x200
.58	156	0x400
59	156	0x600
	Address 0 1 2 3 4 5 6 7	Address  0 0 1 0 2 0 3 0 4 4 5 4 6 4 7 4

Fig. 2(1)

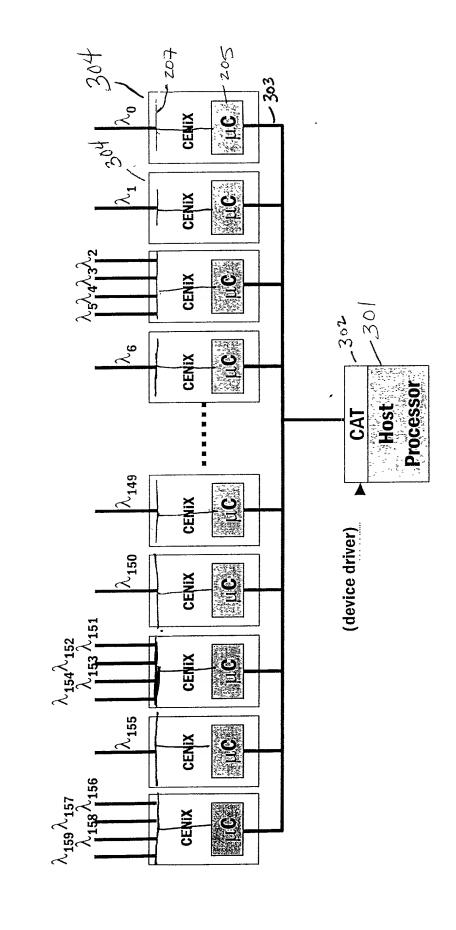


Fig. W

Fig. 4

400

## (Channel Access Table Example)

Channel Address	Physical Address	Memory Offset
0	0	0x000
1	1	0x000
2.	2	0x00′0
3	2	0x200
4	2	0x400
5	2	0x600
6	6	0x000
154	151	0x600
155	155	0x000
156	156	0x000
157	156	0x200
158	156	0x400
159	156	0x600

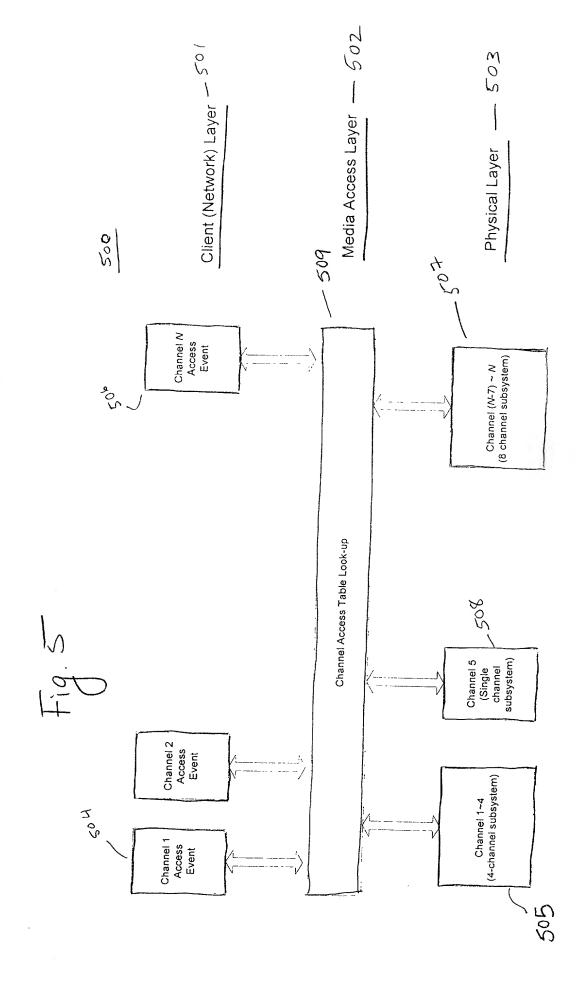


FIG. 6

(Channel Access Table Example)	Channel $Module$ Memory Address Address Offset	0 0 0 0	1 1 0x000	2 2 0x000	3 2 0x200	4 2 0x400	5 2 0x600	000×0 9 9		154 151 0x600	155 155 0x000	156 156 0x000	157 156 0x200	146		Character (Character)	nel Access Tabl nnel (Noduless Address n m m m m m m m m m m m m m m m m m m	ble Example)
	Commar			Find the Module Addressin] contains Channel Address[n]	1				Find the memory offset BBBB for parameter X in  Mb du/e_Address[m]:  AAAA + BBBB				Send converted command to physical layer subsystem:	Command Syntax: M. Linadanata, O	1 CC, (AAAA + BBBB) Alaina	Find the memory offset BBB for parameter X in Memory  Command syntax. ChannelAddress[n]. CC, AAAA, (Value)  Find the MeduleAddress[n] -> MobyleAddress[n]  ChannelAddress[n] -> MobyleAddress[n]  Find the memory offset BBBB for parameter X in  Mb d u (e. Address[m]:  AAAA -> AAAA + BBBB  Send converted command to physical layer subsystem: Command syntax: m. Lidaddress[m]:	Chan Chan Addid Addid Addid Addid Addid 12 12 154 155 156 157 158	Channel Access Tal  Channel  Address  Address  1 1 1 2 2 2 3 2 4 2 5 5 2 6 6 6 6 154 151 155 156 156 156

